

Dorian Kiri

Patent Technical Advisor

Dorian structures patent applications that translate deep semiconductor, AI, and wireless engineering into high-value IP assets for technology companies.

Dorian brings more than 14 years of hands-on engineering and research and development experience directly to bear on patent drafting and prosecution — giving clients a technical collaborator who understands their inventions from the inside out. He works with inventors and in-house counsel to surface the core contributions of each innovation and translate them into precisely structured, examiner-ready applications that hold up under scrutiny.

Dorian's technical range spans semiconductor devices, circuit design, memory architectures, advanced packaging, wireless communication, image processing, artificial intelligence (AI), medical devices, autonomous systems, power distribution, software, operating systems, and IoT technologies. That depth lets him engage fluently with complex disclosures across a wide spectrum of industries without losing the technical nuance that makes a patent defensible.

On the prosecution side, Dorian evaluates prior art, assesses portfolio strength, and advises on filing and investment decisions to support clients' broader IP strategies. He has helped multinational companies coordinate prosecution across multiple jurisdictions and has provided technical assessments to guide long-term portfolio development. He also advises clients on defensive publications as a tool to mitigate competitor filings.

Select Experience

- **Leading memory and storage semiconductor manufacturer.** Drafts patent applications directed to semiconductor devices, integrated circuits, advanced packaging technologies, interconnect architectures, and/or semiconductor manufacturing processes. Works closely with inventors to develop patent protection strategies for innovations supporting next-generation semiconductor products.
- **Global imaging technology company.** Drafts patent applications relating to image sensing technologies, semiconductor devices, and/or imaging systems. Assists in protecting innovations used in consumer and commercial imaging applications.

Education

- Boise State University (B.S., Electrical Engineering, 2004)
- Boise State University (M.Eng., Electrical Engineering, 2015)

- **Leading semiconductor company.** Conducted extensive patent landscape and portfolio analysis relating to semiconductor packaging technologies, including advanced package architectures, interconnect structures, and heterogeneous integration techniques. Reviewed and analyzed thousands of patent applications and patents to identify technology trends, intellectual property risks, and opportunities for patent development.
- **Global consumer technology company.** Drafted patent applications relating to artificial intelligence, machine learning, consumer electronics, and software technologies. Worked with inventors across multiple technical disciplines to prepare patent applications covering AI-enabled systems, smartphone technologies, hardware architectures, and computer-implemented innovations.

Languages

- Italian
- Albanian

Charitable & Civic Involvement

- Member, Institute of Electrical and Electronics Engineers (IEEE)
- Past Chair, IEEE Boise Section
- Member, IEEE Eta Kappa Nu (HKN) Honor Society
- Member, Boise State University Founder Leadership Society